

UTILITY/	11/17/97	2 Cpl.
SERIAL	250	Subclass
ISSUE CLASSIFICATION		
SEARCHED	[Redacted]	

UTILITY SERIAL NUMBER PATENT DATE SEP 14 1999 PATENT NUMBER 5952846

6952846

5952846

SERIAL NUMBER	FILING DATE	CLASS	SUBCLASS	GROUP ART UNIT	EXAMINER
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APPLICANTS
None

None

10.

yes United Kingdom 96269725 Dec. 27, 1996

10.

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	AS FILED	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP. CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO.
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Verified and Acknowledged Examiner's Initials

ADDRESS

A Low Temperature Bump-Bonding Semiconductor Imaging Device

TITLE

5/16/97

U.S. DEPT. OF COMM / PAT. & TM PTO-436L (Rev.12-94)

PARTS OF APPLICATION
FILED SEPARATELY

4/16/97
J. L. [Signature]
Applications Examiner

NOTICE OF ALLOWANCE MAILED

24-9-99

CLAIMS ALLOWED

ISSUE FEE

Amount Due 1005.00 Date Paid 10/10/96

Total Claims	Print Claim
30	1

DRAWING

Sheets Drwg.	Figs. Drwg.	Print Fig.
3	6	2

ISSUE BATCH NUMBER

R57

Label Area

Primary Examiner

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Form PTO-436A
(Rev. 8/92)

SCAN 8 C6
QC MC Formal Drawings () this set

(FACE)

ISSUE FEE IN FILE